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ABSTRACT OF THE DISCLOSURE

A method of automatically activating a clock master circuit in a stack of repeaters includes selectively connecting a first on pin in a first repeater to a voltage source when the first repeater is powered on. The first on pin is connected to a power state output pin in a second repeater, wherein the power state output pin is configured to be connected to ground when the second repeater is powered on. A clock master circuit is enabled based on the voltage on the first on pin.

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